FPGA/CPLD DIL MODULE

Introduction

The GOP module series are mini-modules composed of a CPLD device with a PAL/GAL compatible 24 pin DIL footprint.

Overview

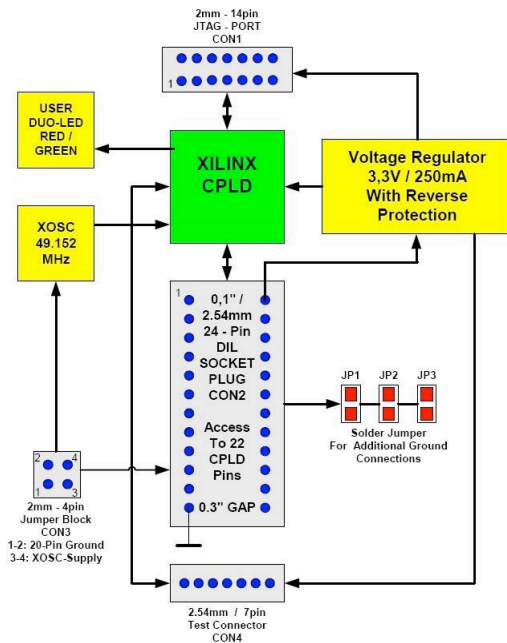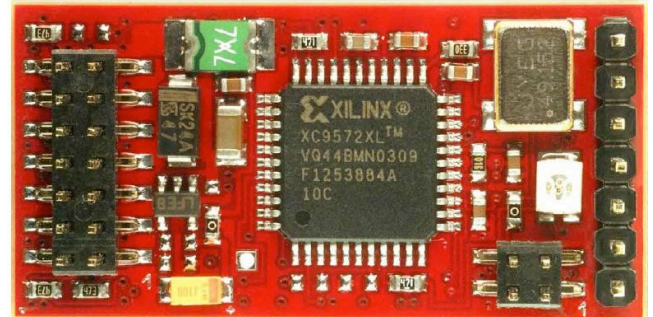

Figure 3: GOP_CPLD board overview

Figure 4: CPLD GOP DIL-module

Features

- Onboard clock oscillator for audio or RS232 applications
- Solder jumpers for additional ground connections
- Programmable ground I/O pins decrease ground bounce problems
- Inputs are 5V tolerant (except for XC2C64)

Applications

- Upgrade from PAL/GAL devices
- Fast evaluation of Xilinx CPLD's
- Rapid Prototyping
- Battery operated equipment
- Hardware Platform for VHDL/VERILOG logic design courses
- Battery operated equipment

Specifications

Product	GOP_XC9572XL	GOP_XCR3064XL	GOP_XC2C64
Xilinx CPLD	XC9572XL-10VQ44C	XCR3064XL-10VQ44C	XC2C64-7VQ44C
Clock	49.152 MHz		
Signal	1 user dual led red/green		
Power Supply	3.5V to 5.5V (on Pin24)		2.7V to 3.6V
Dimensions	40.5mm x 20mm		
Weight	7g		