Engineering Change Notice (ECN)

Change Description

Due to add an I2C interface to the original PA1010D module, the new version have launched and the original(v.01) PA1010D module will not be manufactured anymore. Instead, CDTop will make a pin location change on the new(v.02) PA1010D module. This ECN will compare the differences in characteristics and other related specifications between the new PA1010D module and the original one.
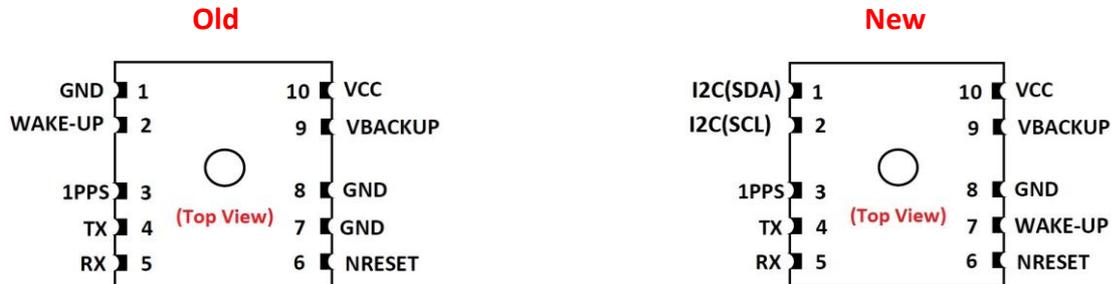
Result

After related checks were completed, we found that the new PA1010D module only differs in appearances and looks, while the performance and other characteristics remained the same as the original one. Please refer below for information on the differences between the original and new PA1010D module.

Difference (Change)

1. **Pin location:**

   a. The new one has a **I2C** interface on the pin 1 & 2. The pin1 is defined to **I2C_SDA**, it outputs GPS information for I2C application, keep floating if not used. Pin 2 is defined to **I2C_SCL**, it is used to received the clock for I2C application, keep floating if not used.

   b. The **WAKE-UP** pin shift from pin2 to pin 7.

2. **Firmware:**

   The firmware burn in the new PA1010D(v.02) module different with original one(v.01).

Phase out plan:

CDTop will phase out the original PA1010D(v.01) by the end of July, 2019. If customer want to continue used the original version please inform us as soon as possible, CDTop will keeping original PCB for them. If the current user interesting in the new version PA1010D, CDTop will provide free sample that same quantity with previously ordered by the customer to them.